

**STATUS OF THE CLAIMS**

The status of the claims of the present application stands as follows:

Claims 1-5 (**Canceled**).

6. (**Original**) An inductor formed on an integrated circuit chip, the inductor comprising:  
two or more outer layers each including a first portion and a second portion;  
one or more inner layers between said two or more outer layers;  
inductor metal winding turns included in said one or more inner layers; and  
a series of magnetic metallic strips disposed on each of said first and second portions of said  
two or more outer layers and on each of said one or more inner layers, said series of  
magnetic metallic strips on said first portion and said second portion arranged so as to  
form a particular pattern.
7. (**Original**) An inductor according to claim 6, wherein said particular pattern is a perfect  
orthogonal grid pattern.
8. (**Original**) An inductor according to claim 6, wherein said particular pattern is a transverse  
grid pattern.
9. (**Original**) An inductor according to claim 6, wherein said series of magnetic metallic strips  
substantially surrounds said inductor metal winding turns.
10. (**Original**) An inductor according to claim 6, further comprising gaps between each of said  
two or more outer layers and said one or more inner layers.

Claims 11-22 (**Canceled**).

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